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## \*BIBDATASHEET\*

Bib Data Sheet

**CONFIRMATION NO. 2677** 

| SERIAL NUMBER<br>10/040,608  | FILING DATE<br>12/28/2001<br>RULE                              |  | CLASS<br>711      | GROUP ART L<br>2186 | JNIT  |  | RNEY DOCKET<br>NO.<br>P10720 |  |
|--|--|--|-------------------|---------------------|---|--|------------------------------|--|
| APPLICANTS   |  |  |                   |                     |   |  |                              |  |
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| ** CONTINUING DATA **********************************  |  |  |                   |                     |   |  |                              |  |
| ** FOREIGN APPLICATIONS ************************************   |  |  |                   |                     |   |  |                              |  |
| IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 02/07/2002  |  |  |                   |                     |   |  |                              |  |
| Foreign Priority claimed yes no  |  |  | STATE OR          | SHEETS              | TOTAL   |  | INDEPENDENT                  |  |
| 35 USC 119 (a-d) conditions me yes no Met after Allowance  Verified and Acknowledged Examiner's Signature Initials |  |  | COUNTRY<br>ISRAEL | DRAWING<br>3        | CLAIMS<br>30  |  | CLAIMS<br>4                  |  |
| ADDRESS  David J. Kaplan Intel Corporation, SC4-202 2200 Mission College Blvd Santa Clara, CA 95052                |  |  |                   |                     |   |  |                              |  |
| TITLE<br>Method and apparatus for communicating between integrated circuits in a low power mode                    |  |  |                   |                     |   |  |                              |  |
| No.  | No to charge/credit DEPOSIT ACCOUNT RECEIVED No for following: |  |                   |                     | All Fees  1.16 Fees (Filing)  1.17 Fees (Processing Ext. of time)  1.18 Fees (Issue)  Other  Credit |  |                              |  |